EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	413	parallel near3 load$3 with enable	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L3	329	parallel with load$3 near2 configuration	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L4	0	parallel with load$3 near2 configuration same enable near2 line	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L7	1076	block with enable FPGA	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L8	0	block with enable near2 load$3FPGA	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L11	413	parallel near3 load$3 with enable	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L12	0	parallel near3 load$3 with enable same configuration near2 controller	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L15	133	enable near3 load$3 near6 block$1	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L17	707	enable near2 (line$1 OR signal$1) with (configuration OR memory) near2 block$1	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25
L21	137	enable near2 load$3 near8 block	US-PGPUB; USPAT; USOCR	AND	OFF	2007/02/20 07:25
L22	152	enable near2 load$3 near8 block$1	US-PGPUB; USPAT; USOCR	AND	OFF	2007/02/20 07:25
L24	466	FPGA with load$3 near4 configuration	US-PGPUB; USPAT; USOCR	AND	OFF	2007/02/20 07:25
L25	785	FPGA same load$3 near4 configuration	US-PGPUB; USPAT; USOCR	AND	OFF	2007/02/20 07:25
L26	140	FPGA same load$3 near4 configuration block with enable	US-PGPUB; USPAT; USOCR	AND	OFF	2007/02/20 07:25
L33	527	parallel near3 load$3 with configuration	US-PGPUB; USPAT	AND	OFF	2007/02/20 07:25